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| To: | Examiner Christian Chase | From: | Fred G. Pruner, Jr. |
| Company: | U.S. Patent & Trademark Office | Date: | August 22, 2005 |
| Fax: | (571) 273-8300 | Pages: | 5 (including coversheet) |
| Your Re: | 10/038,960 | Our Re: | ITL.0668US (P12986) |

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MESSAGE:

Applicant: Jeffrey R. Wilcox et al.
Serial No.: 10/038,960
Filing Date: January 2, 2002
Title: Method And Apparatus For Reducing Power Consumption In A
Memory Bus Interface By Selectively Disabling And Enabling Sense
Amplifiers

1. Reply to Office Action dated June 22, 2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|--------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------|-----------------|---------------------|
| Applicant: | Jeffrey R. Wilcox, et al. | § | Group Art Unit: | 2187 |
| Serial No.: | 10/038,960 | § | Examiner: | Christian Chase |
| Filed: | January 2, 2002 | § | Assignee: | Intel Corporation |
| For: | Method and Apparatus For Reducing Power Consumption In A Memory Bus Interface By Selectively Disabling And Enabling Sense Amplifiers | § § § § § § | Atty. Dkt. No.: | ITL.0668US (P12986) |

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